

PTO/SB/08A (08-03)

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STATEMENT BY APPLICANT
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Sheet 1

of 12

Complete If Known

Application Number	10/666,220
Filing Date	9/19/2003
First Named Inventor	Cho
Art Unit	2818
Examiner Name	Tran, Mai Huong C
Attorney Docket Number	C002-D-2

U. S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number-Kind Code ² (If Known)			
MC	US- 3,943,549	3-9-1976	Jaecklin et al.		
MC	US- 4,165,517	8-21-1979	Temple et al.		
MC	US- 4,281,336	7-28-1981	Sommer et al.		
MC	US- 4,323,793	4-6-1982	Schutten et al.		
MC	US- 4,646,121	2-24-1987	Ogura		
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MC	US- 5,463,344	10-31-1995	Temple		
MC	US- 4,956,688	09-1990	Honma et al.		
MC	US- 6,229,161	5-8-2001	Nemati et al.		
MC	US- 6,104,045	8-15-2000	Forbes et al.		
MC	US- 6,225,165	5-1-2001	Noble, et al.		
	US-				

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T ⁶
		Country Code ³ Number ⁴ Kind Code ⁵ (If Known)				

Examiner Signature

Mai Huong C

Date Considered

03/22/05

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C002-D-2

NON PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
MH		Nemati, Farid and Plummer, James, D., A Novel High Density, Low Voltage SRAM Cell with a Vertical NDR Device, VLSI Technology Technical Digest, 6/1998	
NH		Nemati, Farid and Plummer, James, D., A Novel Thyristor-based SRAM Cell (T-RAM) for High-Speed, Low-Voltage, Giga-scale Memories, International Electron Device Meeting Technical Digest, 1999	
NH		Gribnikov, Z.S., Korobov, V.A., and Mitin, V.V., The Tunnel Diode as a Thyristor Emitter, Solid-State Electronics, Vol. 42, No. 9, 1998, pp. 1761-1763	

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